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APPLICANTS *O.K. TYT*

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** CONTINUING DATA *****
 This application is a DIV of 10/267,365 10/09/2002 PAT 6,713,325 *O.K. TYT*

** FOREIGN APPLICATIONS *****
 JAPAN 2002-000702 01/07/2002 *O.K. TYT*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i> INITIALS <i>TYT</i>			

Verified and Acknowledged

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TITLE
 Method of manufacturing a semiconductor integrated circuit and semiconductor integrated circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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